

Fig. 2

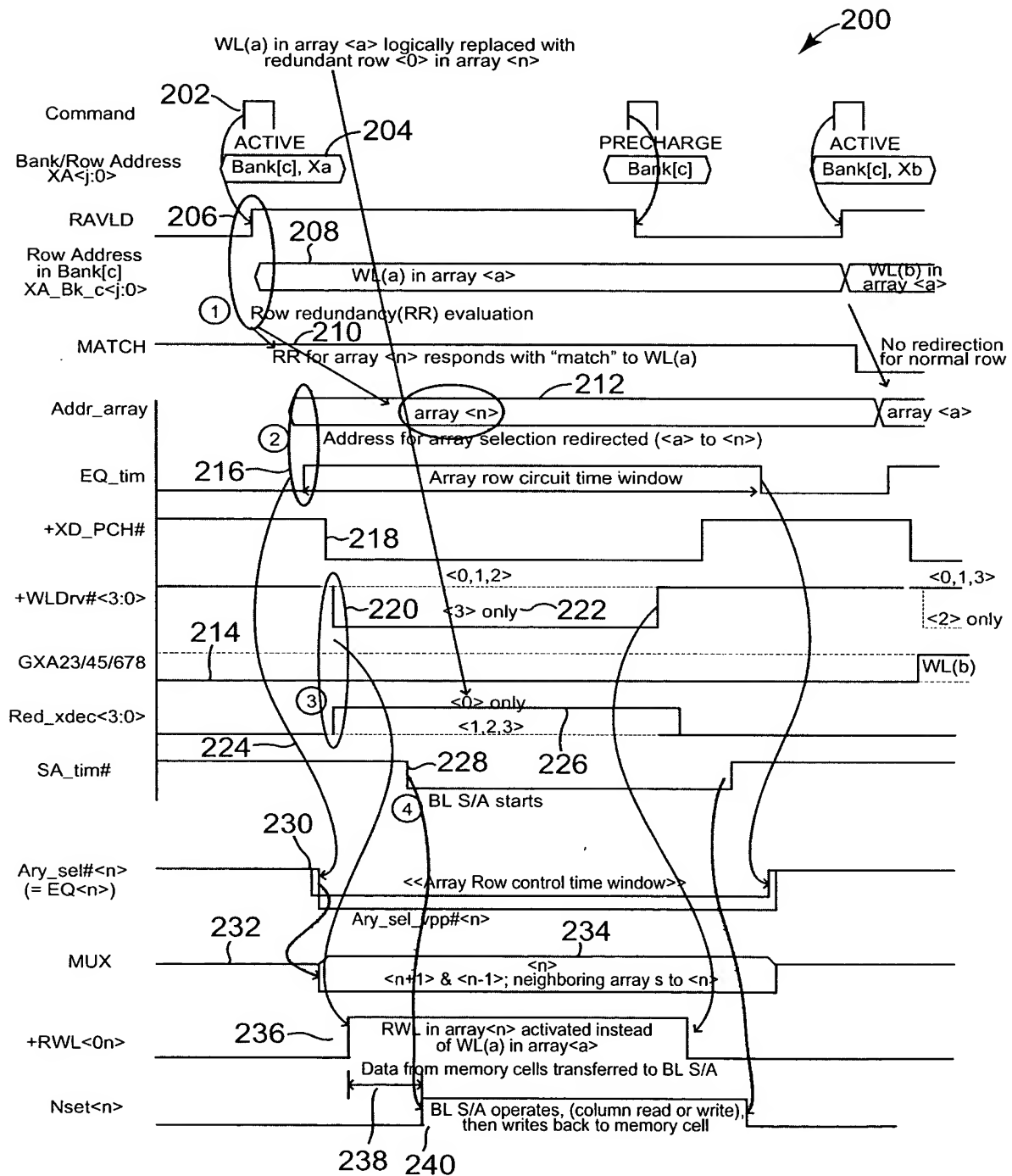


Fig. 3

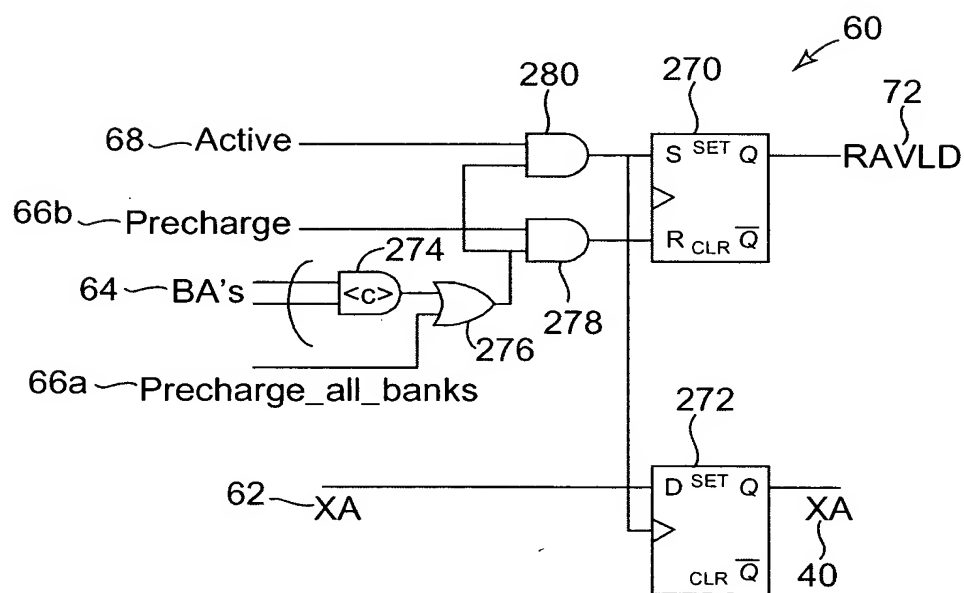


Fig. 4

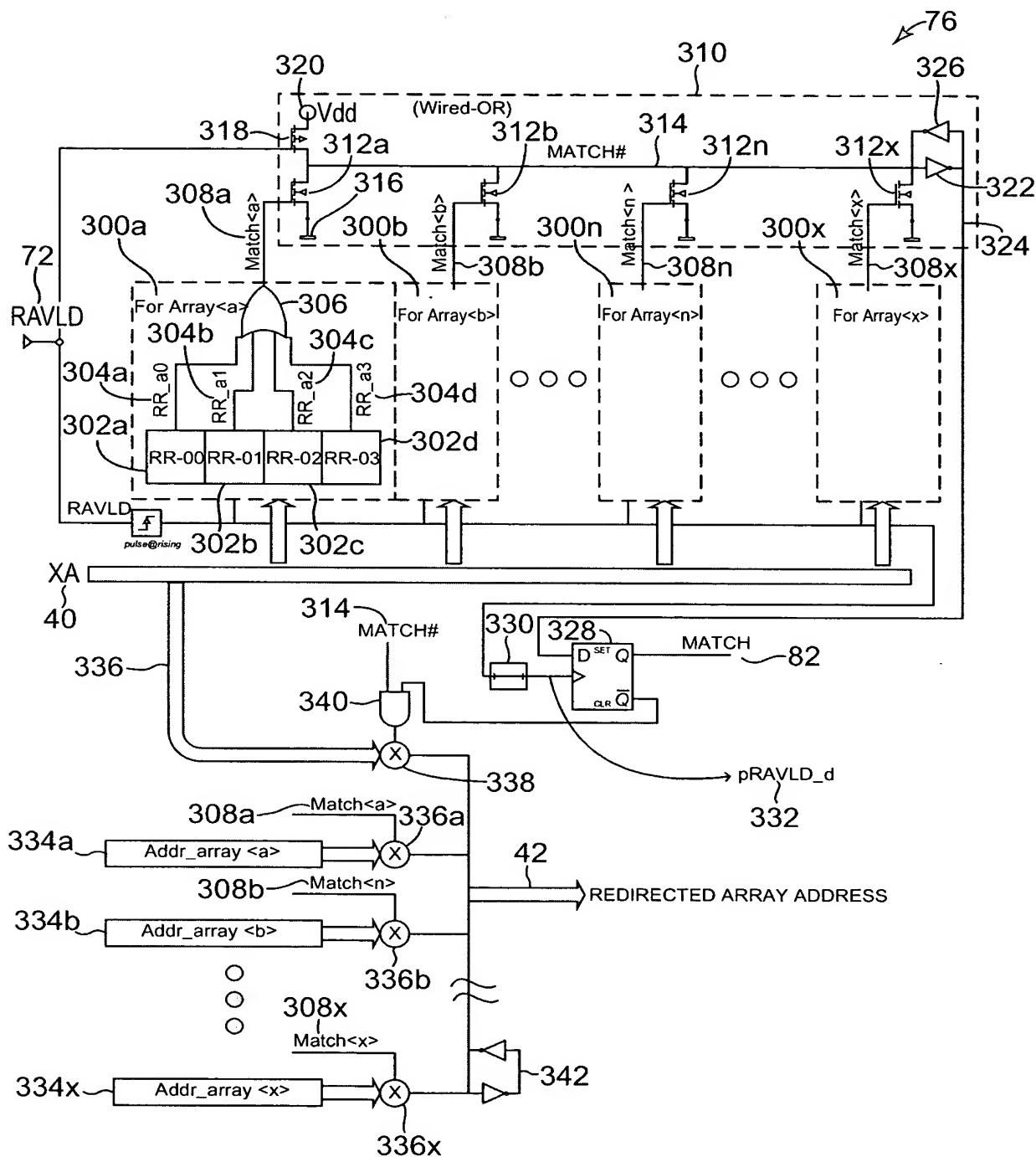
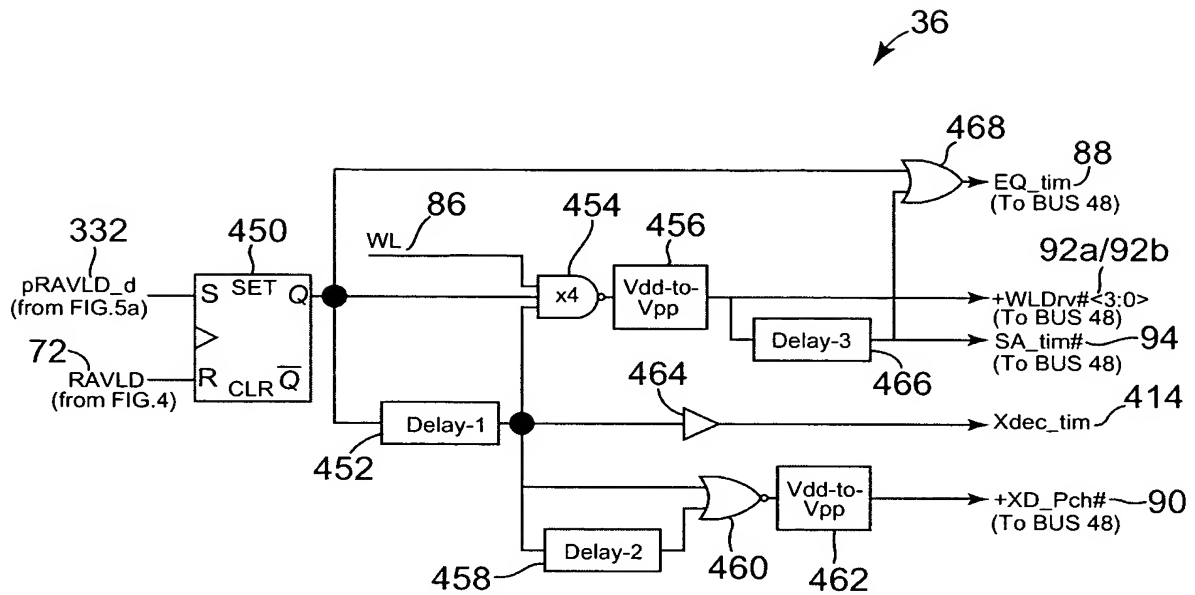


Fig. 5A



470

	L-H delay represents	H-L delay represents
Delay-1	EQ/MUX off to WL On time	WL Off by +WLDrv# to XDEC off time
Delay-2	Not applicable	GXA23/45/678 or Red_xdec<3:0> Off time to precharge enable for XDEC
Delay-3	WL On to full signal transfer to BL S/A	WL Off to EQ/MUX On time

Fig. 6